

<b>INFORMATION DISCLOSURE CITATION FORM FOR PATENT APPLICATION (FORM PTO-1449) (Substitute)</b>			Docket No.: 915-007.146		Serial No.: <b>10/536727</b>	
			Applicant(s): Antti RUHA et al			
			Filing Date: <del>Herewith</del> <b>5/27/05</b>		Group: <b>2816</b>	
<b>U.S. PATENTS</b>						
Initials	Patent Number	Issue Date	Name	Class	Sub-class	Filing date
<i>Kam</i>	5,963,156	10/5/99	Lewicki et al	341	122	
<b>U.S. PATENT PUBLICATIONS</b>						
Initials	Publication No.	Pub. Date	Name	Class	Sub-class	Filing Date
<b>FOREIGN PATENT DOCUMENTS</b>						
Initials	Document Number	Date	Country	Name	Translation? Yes/No/n/a	
<i>Kam</i>	WO 01/24192 A1	4/5/01	WO			
<b>OTHER DOCUMENTS (Title, Author, Date, Pages, Etc., if known)</b>						
<i>Kam</i>	"A Complex Charge Sampling Scheme for Complex if Receivers" by S. Karvonen et al, Proceedings of the 2003 IEEE International Symposium on Circuits and Systems (Cat. No. 03CH37430), ISCAS 2003; International Symposium on Circuits and Systems, Bangkok, Thailand, May 25-28, 2003, pages II-169 – II-172.					
	"A Track & Hold-AGC Suitable for Downconversion by Subsampling" by T. Vasseaux, et al, ICECS'99, Proceedings of ICECS '99; 6 <sup>th</sup> IEEE International Conference on Electronics, Circuits and Systems (Cat. No. 99EX357), ICECS'99; Proceedings of ICECS'99, 6 <sup>th</sup> IEEE International Conference on Electronics, Circuits and Systems, Pafos, Cyprus, pages 1527-1530, 1999.					
	"A Charge Sampling Mixer with Embedded Filter Function for Wireless Applications" by J. Yuan, 2 <sup>nd</sup> International Conference on Microwave and Millimeter Wave Technology Proceedings, 2000					
✓	"High-Speed Low-Power Integrating CMOS Sample-and-Hold Amplifier Architecture, L. Carley et al, Custom Integrated Circuits Conference, 1995					
Examiner's Signature: <i>Kenneth</i>				Date Considered: <b>9/20/06</b>		
Initial if reference was considered, whether or not citation is in conformance with MPEP. Mark through citation if not considered. Include a copy of this citation form with your next correspondence to the Applicant(s).						

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<b>OTHER DOCUMENTS (Title, Author, Date, Pages, Etc., if known)</b>						
✓	"Charge Sampling Mixer with $\Delta\Sigma$ Quantized Impulse Response" by S. Karvonen et al, IEEE International Symposium on Circuits and Systems 2002, volume 1.					
	"Analysis and Realization of a Downconverting Quadrature Sampler", by S. Karvonen, Diploma Thesis, University of Oulu, 2001					
	"CMOS Baseband Integrated Circuit Techniques for Radio Receivers" by S. Lindfors, Doctoral thesis, Helsinki University of Technology, July 2000					
✓	"A Low Noise Quadrature Subsampling Mixer" by S. Karvonen, et al, IEEE International Symposium on Circuits and Systems 2001, Volume 4					
Examiner's Signature: <i>Kenneth W. [Signature]</i>				Date Considered: 9/20/06		
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